Electronic Acknowledgement Receipt					
EFS ID:	1922333				
Application Number:	10079472				
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Title of Invention:	Gate dielectric structures for integrated circuits and methods for make and using such gate dielectric structures				
First Named Inventor/Applicant Name:	Maitreyee Mahajani				
Customer Number:	33971				
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Supplemental Appeal Brief	SDP106US_ReplacementAp pealBrief.pdf	7680607	no	37
Warnings:					

Information	:				
2	Change of Address	SDP106US_ChangeofCorre sAdd.pdf	222818	no	1
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.